

**ABSTRACT OF THE DISCLOSURE**

A liquid crystal display panel. The liquid crystal display panel is driven by a column inversion driving mode and includes a plurality of data lines, a plurality of scan lines, a plurality of display unit pixels and a plurality of display unit blocks. Each set of two adjacent data lines and two adjacent scan lines defines the boundary of one display unit pixel wherein each data line interlaces with both scan lines. Each display unit pixel comprises a control transistor, a storage capacitor, a common electrode, and a pixel electrode. In addition, the display unit pixels within any display unit block between two adjacent data lines are coupled to one of the two adjacent data lines, while the display unit pixels within another adjacent display unit block between the two adjacent data lines are coupled to the other of the two adjacent data lines.